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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			Docket No.
Applicant(s): Malinowski et al.			BUR920000028US1
Application No. 09/683,027	Filing Date 11/9/2001	Examiner Luan C. Thai	Group Art Unit 2891
Invention: DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS			
<p>I hereby certify that this <u>Request for Reconsideration (16 pages)</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>571-273-8300</u>)</p> <p>on <u>12/8/2005</u> (Date)</p> <p><u>Kim Dwileski</u> (Typed or Printed Name of Person Signing Certificate)</p> <p><u>Kim Dwileski</u> (Signature)</p> <p>Note: Each paper must have its own certificate of mailing.</p>			

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/683,027

Applicant(s): Malinowski et al.

Filed.: November 9, 2001

Art Unit: 2891

Dkt. No.: BUR920000028US1

Examiner: Luan C. Thai

**Title: DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE
PROTECTION OF INTEGRATED CIRCUITS**

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed
on September 22, 2005.